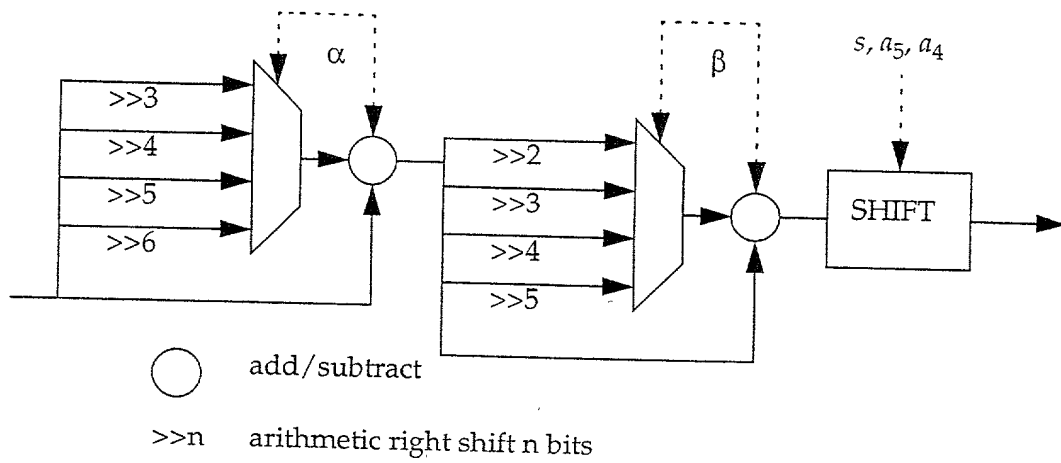Figure 1. A diagram of the new digital AGC.

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Figure 2. An efficient CSD multiplier using (7).